



St.Thomas Institute for Science & Technology, Trivandrum (College code: STI)

Department of Electrical And Electronics Engineering

Timetable for Odd Semester 2024-25

Semester : III

Day/Hour	1 9.00am-10.00 am	2 10.00am-11.00am	Tea Break (11.00 am-11.10 am)	3 11.10am-12.10pm	4 12.10 pm-1.00 pm	Lunch Break (1.00pm-1.40pm)	5 1.40 pm-2.30 pm	6 2.30 pm-3.20 pm	Tea Break (3.20 pm-3.30 pm)	7 3.30 pm-4.20 pm	
DAY 1	CN(PV)	CN(PV)		PDEC	MI(AR)		CML(AR/AJ)			CML(AR/AJ)	
DAY 2	PE(AS)	AE(BRVS)		AE(BRVS) (TUTORIAL)	MI(AR)		AEL(BRVS/VBS)			AEL(BRVS/VBS)	
DAY3	PDEC	AE(BRVS)		CN(PV) (TUTORIAL)	CN(PV) (TUTORIAL)		SE(GP)	MI(AR) (TUTORIAL)		PE(AS)	
DAY 4	CN(PV)	CN(PV)		AE(BRVS)	PDEC		PDEC (TUTORIAL)	LIBRARY		PHYSICAL EDUCATION	
DAY 5	MI(AR)	PDEC		AE(BRVS)	SE(GP)		Lunch Break (1.00 pm-2.30 pm)			MI(AR)	CN(PV)
Acronym	Name of the subject			Course Code	L-T-P	Hours	Hours Allotted	Credit	Faculty In Charge		
PDEC	PARTIAL DIFFERENTIAL EQUATIONS & COMPLEX ANALYSIS			MAT201	3-1-0	4	5	4	Dhanya		
CN	CIRCUITS AND NETWORKS			EET201	2-2-0	4	7	4	Prathibha Viswan (PV)		
MI	MEASUREMENTS AND INSTRUMENTATION			EET203	3-1-0	4	5	4	Asha R (AR)		
AE	ANALOG ELECTRONICS			EET205	3-1-0	4	5	4	Dr.Bibin Raj V.S.(BRVS)		
PE	PROFESSIONAL ETHICS			HUT 200	2-0-0	2	2	2	Dr.Anju S.(AS)		
SE	SUSTAINABLE ENGINEERING			MCN201	2-0-0	2	2	0	Dr.Geenu Paul(GP)		
CML	CIRCUITS AND MEASUREMENTS LAB			EEL 201	0-0-3	3	3	2	Asha R (AR)		
AEL	ANALOG ELECTRONICS LAB			EEL203	0-0-3	3	3	2	Dr.Bibin Raj V.S.(BRVS)		

Timetable Coordinator

Head Of Department

Principal



St.Thomas Institute for Science & Technology, Trivandrum (College code: STI)

Department of Electrical And Electronics Engineering

Timetable for Odd Semester 2024-25

Semester : V

Day/Hour	1 9.00am-10.00 am	2 10.00am-11.00am	Tea Break (11.00 am-11.10 am)	3 11.10am-12.10pm	4 12.10 pm-1.00 pm	Lunch Break (1.00pm-1.40pm)	5 1.40 pm-2.30 pm	6 2.30 pm-3.20 pm	Tea Break (3.20 pm-3.30 pm)	7 3.30 pm-4.20 pm
Day1	SAS(BRVS)	SAS(BRVS)		MP&MC(JAJ)	PS-1(RSM)		SIM(PV)	LIBRARY		PHYSICAL EDUCATION
Day2	SIM(PV)	MP&MC(JAJ)		MP&MC(JAJ)	SIM(PV)		PS-1(RSM) (TUTORIAL)	SIM(PV) (TUTORIAL)		MFE
Day3	MFE	MP&MC(JAJ)		MP&MC(JAJ) (TUTORIAL)	SAS(BRVS)		MP&MCL(JAJ/MSR)			MP&MCL(MSR)
Day4	PS-1(RSM)	MP&MC(JAJ)		DM(VS)	DM(VS)	EML-II(PV/MSR/AJ)		EML(PV/MSR/AJ)		
Day5	PS-1(RSM)	SIM(PV)		PS-1(RSM)	MFE	Lunch Break (1.00 pm-2.30 pm)		SAS(BRVS)		SAS(BRVS) (TUTORIAL)
Acronym	Name of the subject		Course Code	L-T-P	Hours	Hours Alloted	Credit	Faculty In Charge		
PS-I	POWER SYSTEMS-I		EET301	3-1-0	4	5	4	Rinu Sarah Mathews (RSM)		
MP&MC	MICROPROCESSORS & MICROCONTROLLERS		EET303	3-1-0	4	6	4	Jisha Ann Jacob (JAJ)		
SAS	SIGNALS & SYSTEMS		EET305	3-1-0	4	5	4	Dr.Bibin Raj V.S.(BRVS)		
SIM	SYNCHRONOUS & INDUCTION MACHINES		EET307	3-1-0	4	5	4	Prathibha Viswan (PV)		
ME	MANAGEMENT FOR ENGINEERS		HUT310	3-0-0	3	3	3	Karthika		
DM	DISASTER MANAGEMENT		MCN301	2-0-0	2	2		Vyshna S.(VS)		
MP&MCL	LAB		EEL331	0-0-3	3	3	2	Jisha Ann Jacob (JAJ)		
EML-II	ELECTRICAL MACHINES-II LAB		EEL333	0-0-3	3	3	2	Prathibha Viswan (PV)		

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St.Thomas Institute for Science & Technology, Trivandrum (College code: STI)

Department of Electrical And Electronics Engineering

Timetable for Odd Semester 2024-25

Semester : VII

Day/Hour	1 9.00am-10.00 am	2 10.00am-11.00am	Tea Break (11.00 am-11.10 am)	3 11.10am-12.10pm	4 12.10 pm-1.00 pm	Lunch Break (1.00pm-1.40pm)	5 1.40 pm-2.30 pm	6 2.30 pm-3.20 pm	Tea Break (3.20 pm-3.30 pm)	7 3.30pm-4.20pm	
DAY1	ACT(AR)	IT(RSM)		ISE	OE		CSL(RSM/MSR)			CSL (RSM/MSR)	
DAY2	IT(RSM)	ACT(AR)		OE	IT(RSM)		ISE (TUTORIAL)	LIBRARY		PE	
DAY3	ISE	IT(RSM)		ACT(AR)	IT(RSM) (TUTORIAL)		ACT (R)	OE(R)		ISE(R)	
DAY4	OE (TUTORIAL)	ACT(AR)		IT(RSM)	ACT(AR) (TUTORIAL)		SEMINAR(BRVS)			SEMINAR(BRVS)	
DAY5	PROJECT										
Acronym	Name of the subject			Course Code	L-T-P	HOURS	HOURS ALOTTED	CREDIT	Faculty In Charge		
ACS	ADVANCED CONTROL SYSTEMS			EET401	2-1-0	3	6	3	Asha R (AR)		
IT	ILLUMINATION TECHNOLOGY			EET463	2-1-0	3	6	3	Rinu Sarah Mathews(RSM)		
OE	OPEN ELECTIVE				2-1-0	3	4	3	Ashish Prasad(AP)/Bobby John/Shinu Mamachan		
ISE	INDUSTRIAL SAFETY ENGINEERING			MCN401	2-1-0	3	4		Vyshna S.(VS)		
CSL	CONTROL SYSTEMS LAB			EEL411	0-0-3	3	3	2	Rinu Sarah Mathews(RSM)		
SEM	SEMINAR			EEQ413	0-0-3	3	3	2	Dr.Bibin Raj V.S.(BRVS)		
PROJECT	PROJECT PHASE I			EED415	0-0-6	6	6	2	Dr.Bibin Raj V.S.(BRVS)		

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